

Replacing the Transistor with a Lower Voltage Switch: What are the Prospects?

Green Photonics Symposium at Technion Haifa, Israel, April 23, 2014 Eli Yablonovitch, E³S Director

UC Berkeley-Florida International-MIT-Stanford-U Texas El Paso Contra Costa-LA Trade Tech









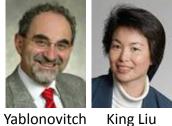




Research Team

U C Berkeley









Theme Leaders



Alon













Massachusetts **Institute of Technology**















Antoniadis

Bulovic

Del Alamo

Fitzgerald

Hoyt

Lang

Swager

Stanford University



The University of Texas at El **Paso**

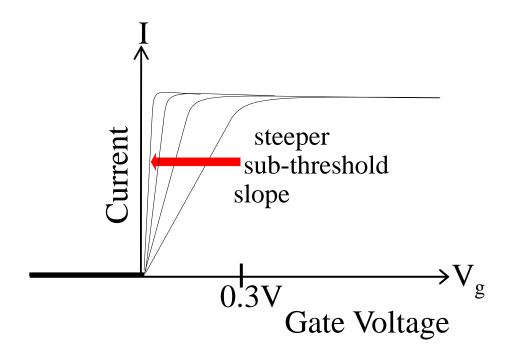


Florida International University





A New More Sensitive Electronic Switch



Take the powering voltage from ~ 1 Volt down to milli-Volts (noise is in μ Volts)

The New Switch has to Satisfy Three Specifications:

Steepness (or sensitivity)
 switches with only a few milli-volts
 60mV/decade ⇒ 1mV/decade

2. On/Off ratio. $10^6:1$

3. Current Density or Conductance Density (for miniaturization)

old spec at 1Volt: 1 mAmp/micron

our spec: 1 milli-Siemen/micron

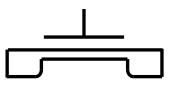
A 1 micron device should conduct at $1K\Omega$ in the on-state.



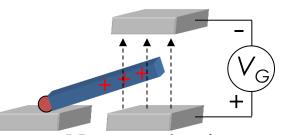


This Center will address the Electronics Energy Efficiency by Four Interconnected Approaches:

Theme 1:

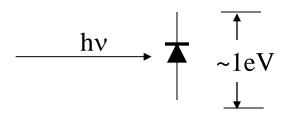


Nanoelectronics: Solid-State Milli-Volt Switching Theme 2:



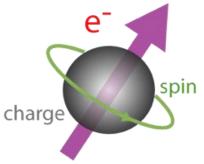
Nanomechanics: Zero-Leakage Switching

Theme 3:



Nanophotonics for Ultra-Low Energy Communication

Theme 4:



Nanomagnetics: A Low Energy Magnetic Switch

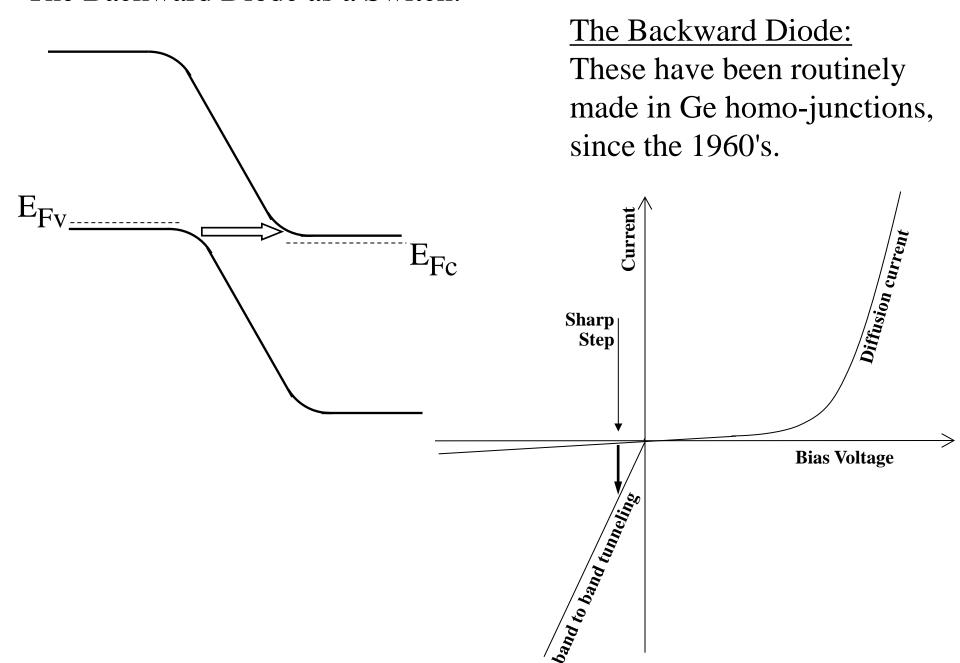


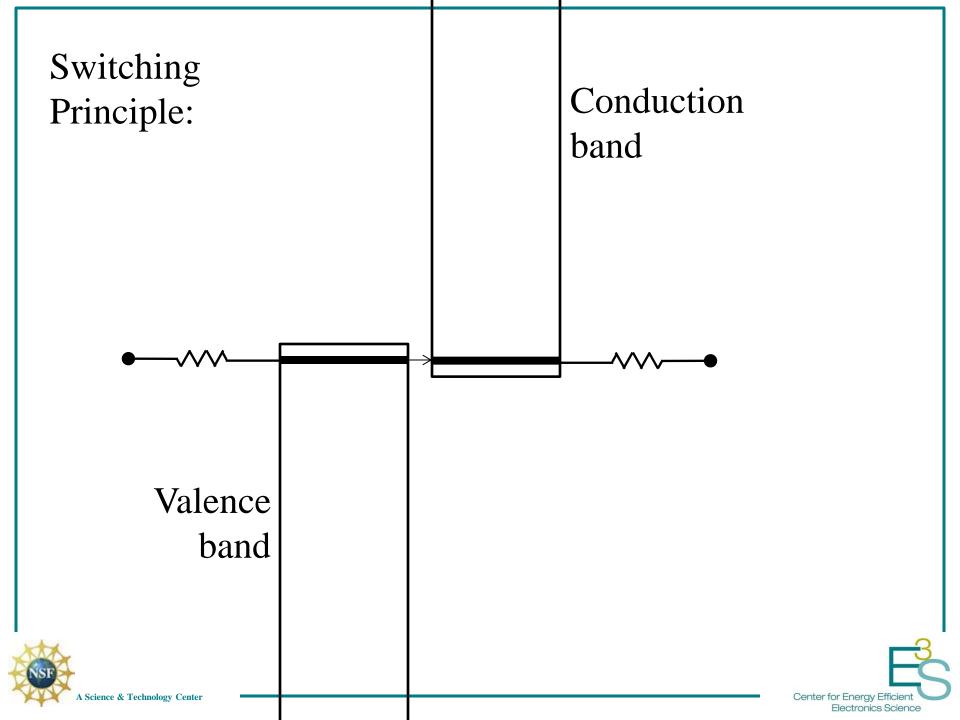
Theme 1 - Nanoelectronics: Solid-State Milli-Volt Switching

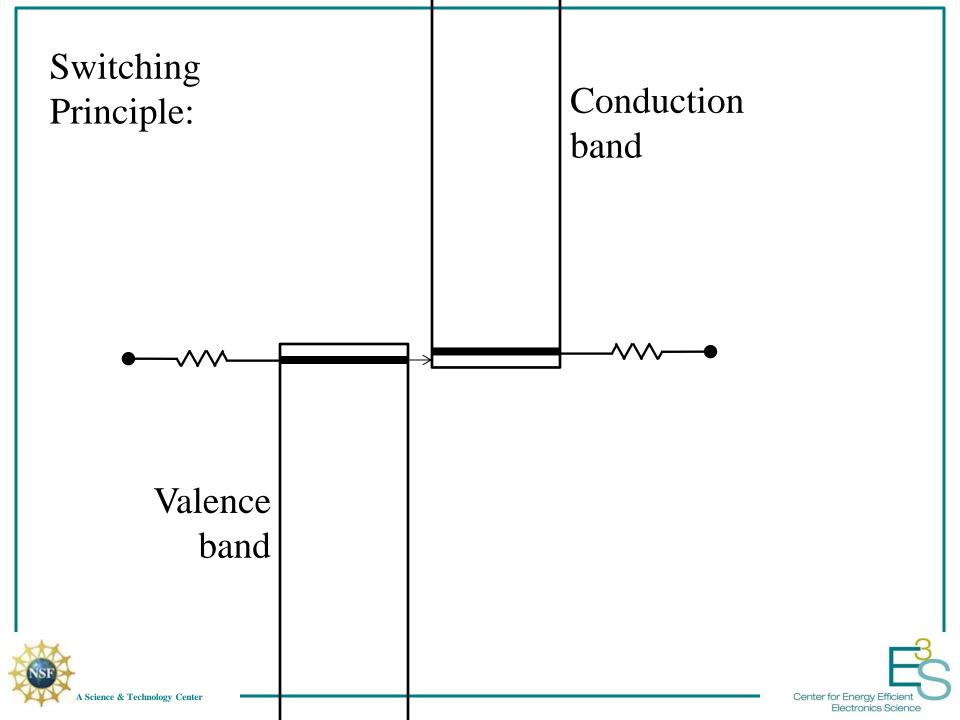




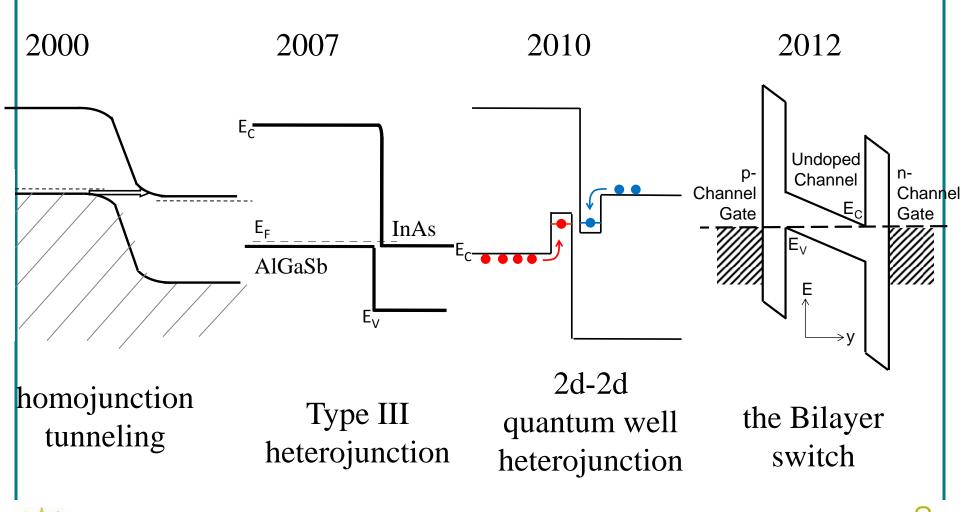
The Backward Diode as a Switch:







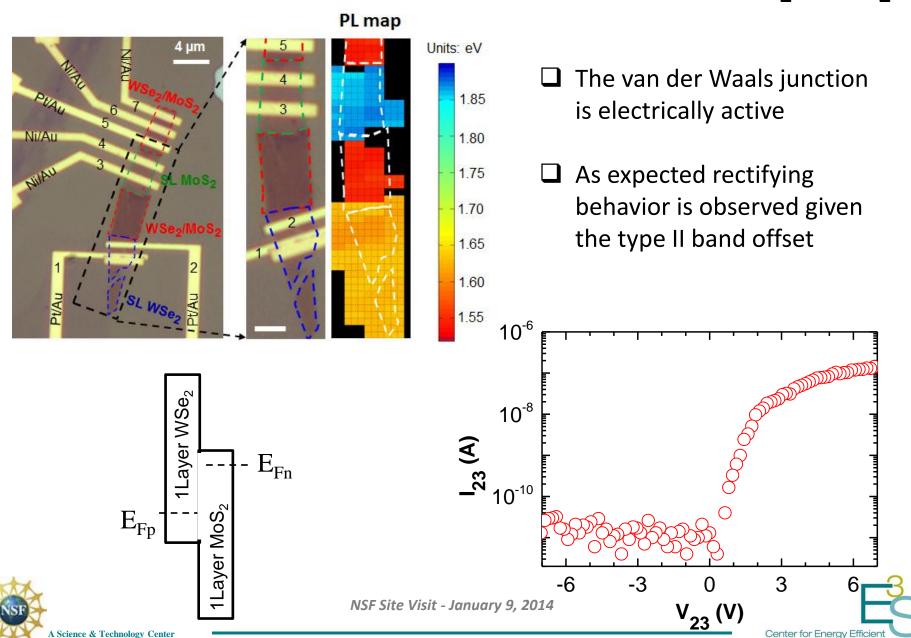
The evolution of the tunnel transistor concept:





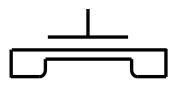


Electrical transport through the heterobilayer WSe₂/MoS₂

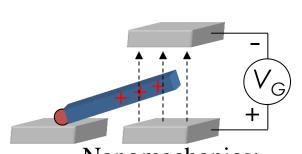


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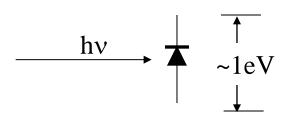


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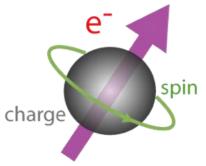
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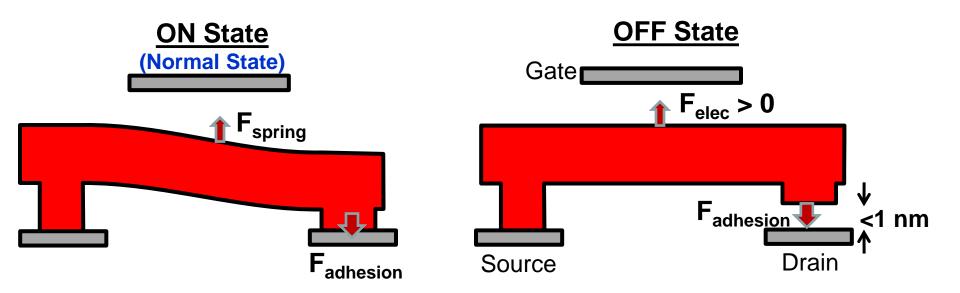


Theme 2 – Nano-Mechanical Switching



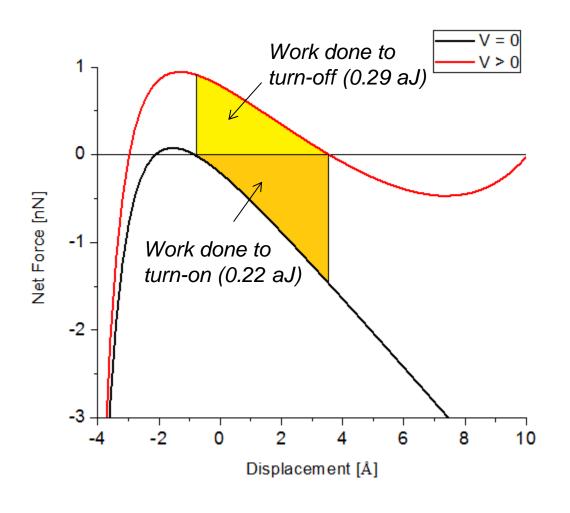


Balance the Spring Constant versus Surface Adhesion



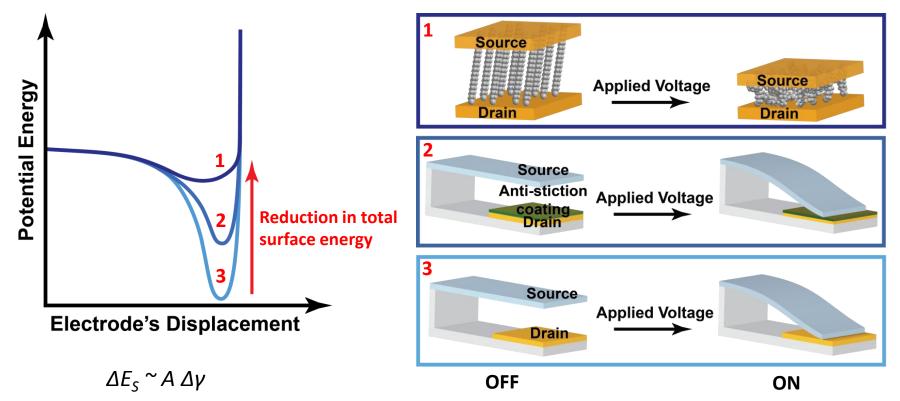
- Contact gap is very small → pull-in upon release
 - A normally-on switch
- Build a gate to turn the switch off electrostatically
 - $-F_{spring}$ helps overcoming $F_{adhesion} \rightarrow$ voltage reduction!

Balance the Spring Constant versus Surface Adhesion



- $g_{cont} = 4 \text{ Å}$
- $g_{act} = 2 \text{ nm}$
- $k_{spring} = 3.5 \text{ N/m}$
- $H = 7 \times 10^{-20} \text{ J}$
 - Assuming SiO₂
- $A_c = 10 \text{ nm}^2$
 - Can be scaled further
 - E_{adhesion} ~ 0.7 aJ
- $A = 0.08 \, \mu m^2$
 - C = 0.41 fF
 - Tradeoff vs. g_{act}

Insert a thin molecular layer to control stiction



 ΔE_s : Surface Energy A: Contact Area $\Delta \gamma$: Work of Cohesion

Physical and chemical interactions causing solid-solid surface adhesion (cohesion)

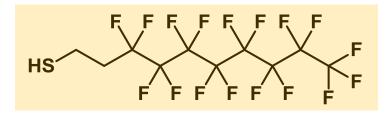
- Covalent
- Ionic
- Electrostatic

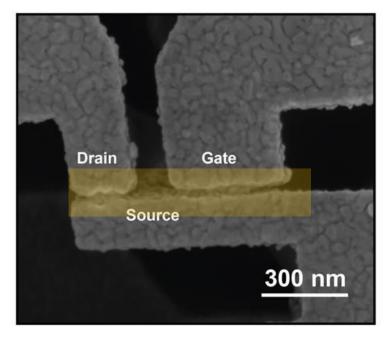
- Metallic
- Hydrogen
- van der Waals



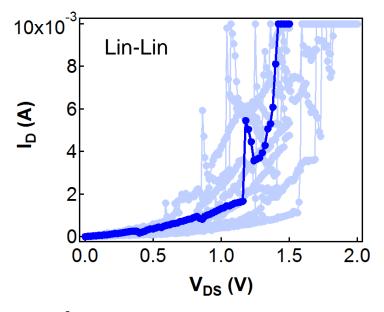
Center for Energy Efficient Electronics Science

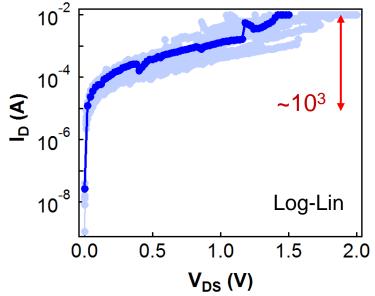
Overcoming Stiction using Organic Thin Film





Organic thin film prevents stiction allowing repeatable operation



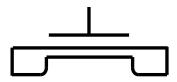




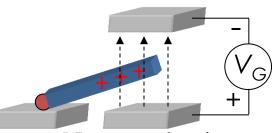
Center for Energy Efficient
Electronics Science

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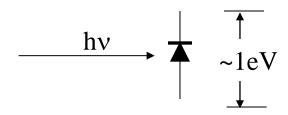


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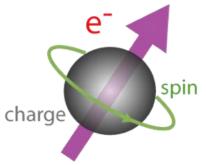
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Theme 4 – NanoPhotonic Switching



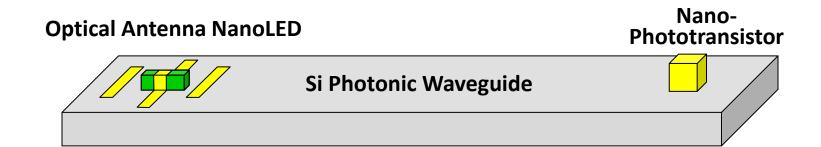


Theme 3

Optical Inter-Connect

Main Goal:

 Dramatically improve the interconnect energy efficiency: 20,000photons/bit ⇒ 20 photons/bit



Key scientific challenges

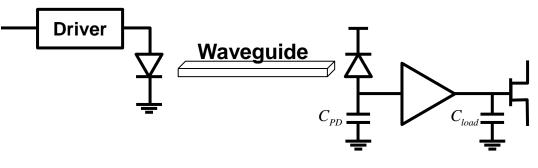
- sub-wavelength-scale nanophotonic devices
- An integrated optical antenna
- Heterogeneous integration of III-V on Si

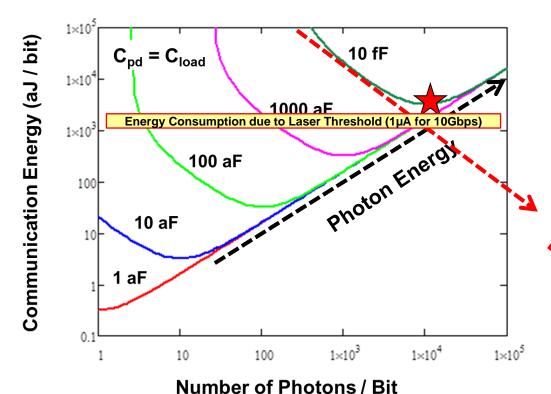






Interconnect Energy





- Energy consumption minimized when transmitter (photon) energy equals receiver (amplifier) energy
- Minimum energy is proportional to detector capacitance
 - → Nanophotodetectors
 (Capacitance ∞ device size)
- Energy consumption due to laser threshold is nonneglibible

→ NanoLED

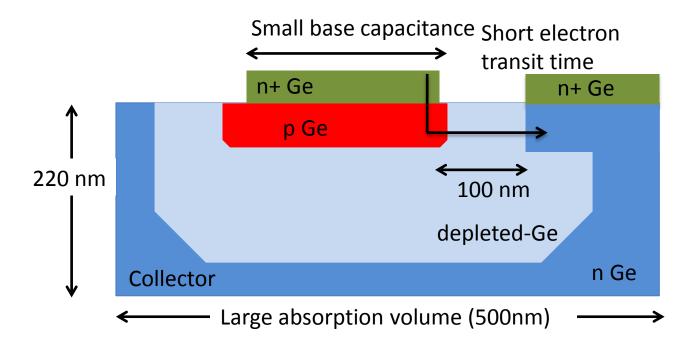
Amplifier Energy



Silicon Photonics: Replace Photodiode with Bipolar Phototransistor

Decoupling light absorption and transit time

- **Chris Keraly**
- This allows for a large device for light absorption, while enabling a small transistor area for low capacitance, large gain and speed



Estimated base capacitance ~ 60 aF

$$C_{Self} + C_{EB} + C_{EC}$$

Electron transit time: 2.5 ps

$$\tau = \frac{W_B^2}{2D_{nh}} + \frac{W_B}{v_{sat}} + \frac{W_{CBD}}{2v_{sat}}$$

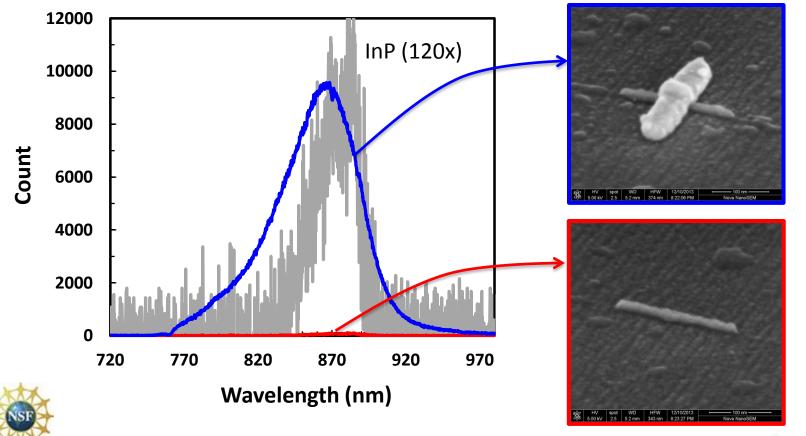


Optical Antenna Based InP NanoLED

- InP has low surface recombination velocity
- Higher photon energy allows direct speed characterization
- 120x enhancement with 17nm wide ridge



Kevin Messer



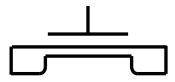
InP NanoLED

InP

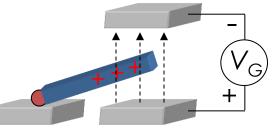


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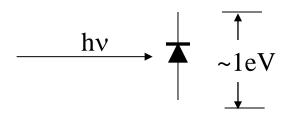


Nanoelectronics: Solid-State Milli-Volt Switching Theme 2:



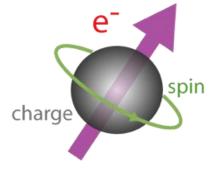
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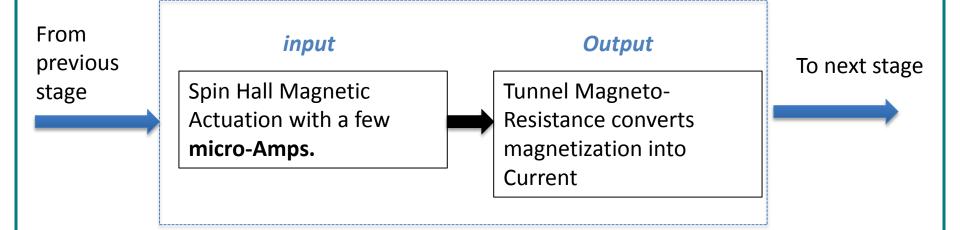


Center for Energy Efficient

Electronics Science

Theme 4 – NanoMagnetic Switching

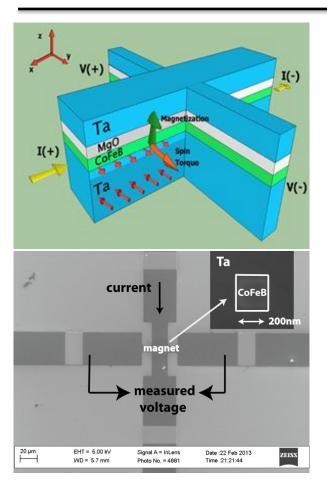
Nano-Magnetic Switching: Gain & Fan-out

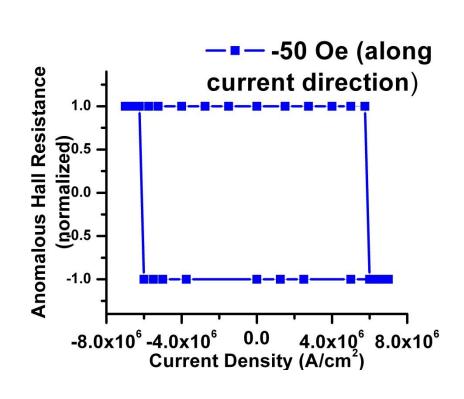






Spin Hall Effect observed by Salahuddin et al prior to Cornell publication



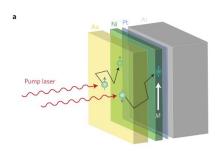


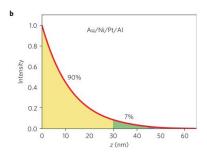
Bhowmik, You and Salahuddin, IEDM, 2012, MMM, 2013; Also Liu et. al. Science (2012)



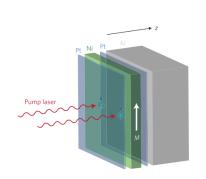


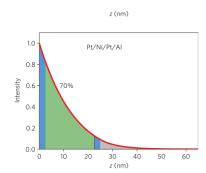
High Speed Magnetic Switching: Demagnetization by Electrical Hot Electron Injection OR by Rapid Electrical Thermal Heating



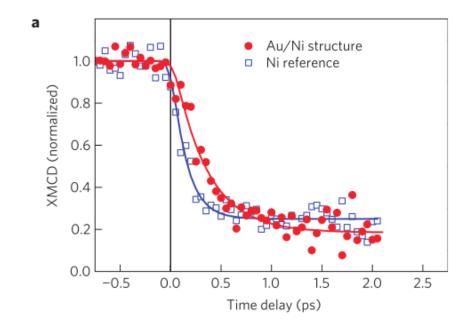


Laser absorbed in Au layer Hot electrons "super" diffuse into Ni





Laser absorbed directly in Ni



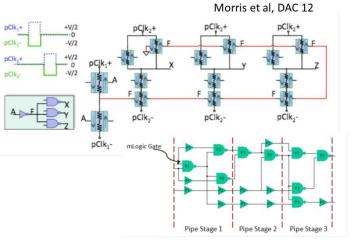
Ni magnetization dynamics

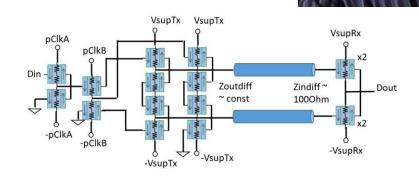




Circuits/Systems Design with Modest On/Off Ratio Magnetic Devices

New Investigator: Vladimir Stojanovic, UCB





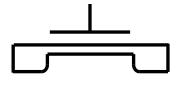
- Pulse gates to save on leakage pipelining a useful side benefit
 - Need to evaluate savings with detailed study of pulsing costs (CMOS chip)
- Instead use multi-phase clocks/pulses for stage-gating





A low-voltage technology, or an impedance matching device, needs to be invented/discovered at the Nano-scale:

photo-diode

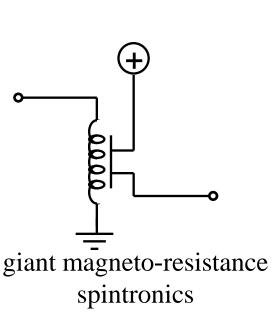


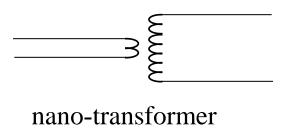
transistor amplifier with steeper sub-threshold slope

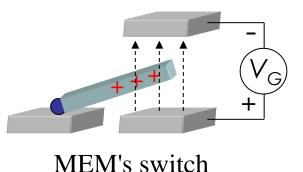
- •TFET's
- •Negative Capacitance Gates
- --V₂O₅ metal-insulator transition

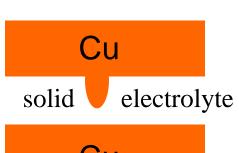


Cryo-Electronics kT/q~q/C



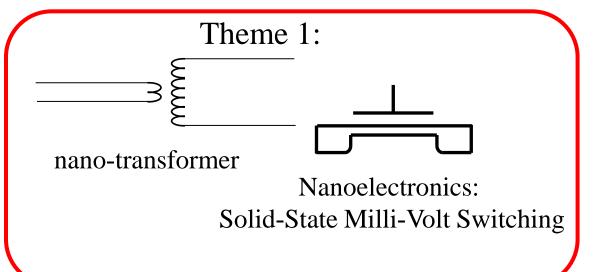




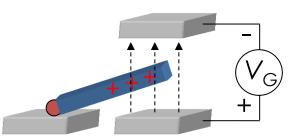




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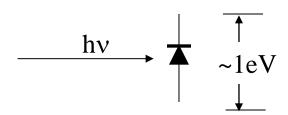


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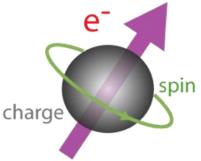
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